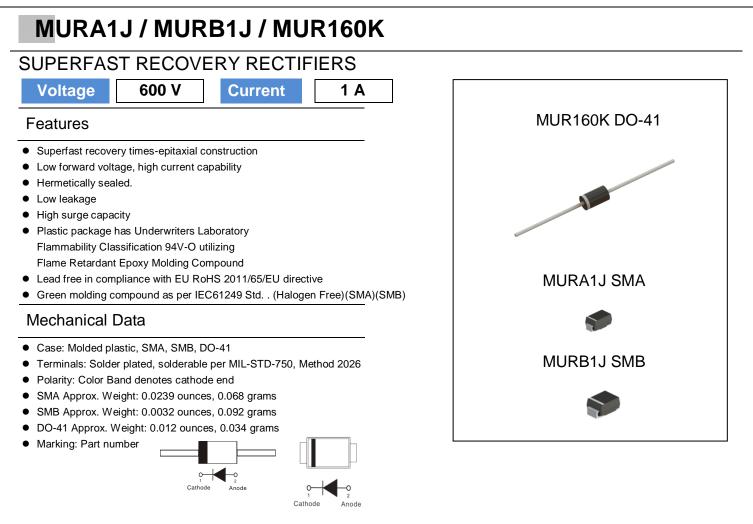
ΡΛΝ	JIT	
	SEMI	
	CONDUCTOR	





Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Maximum repetitive peak reverse voltage	Vrrm	600	V
Maximum rms voltage	Vrms	420	V
Maximum dc blocking voltage	Vr	600	V
Maximum average forward current	lf(AV)	1	А
Peak forward surge current : 8.3ms single half sine- wave superimposed on rated load	IFSM	30	А
Typical forward voltage at 1A	VF	1.25	V
Maximum dc reverse current at rated dc blocking voltage	lr	5	μΑ
Operating and storage temperature range	TJ, TSTG	-55 to +175	°C



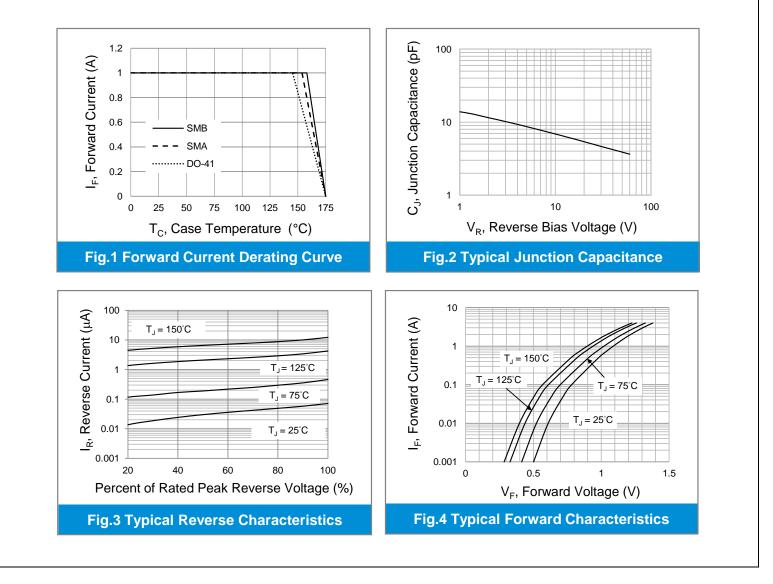
MURA1J / MURB1J / MUR160K

PARAMETE	R	SYMBOL	LIMIT	UNIT
Maximum reverse recovery time	(Note 4)	Trr	50	ns
Typical thermal resistance	SMA(Note 3)	$R_{ extsf{ heta}JA}$	150	
	SMB(Note 3)	$R_{ extsf{ heta}JA}$	135	
	SMA(Note 1)	$R_{ extsf{ heta}JC}$	25	°C/W
	SMB(Note 1)	$R_{ extsf{ heta}JC}$	20	
	DO-41(Note 2)	$R_{ extsf{ heta}JL}$	55	

Maximum Ratings ($T_A=25^{\circ}C$ unless otherwise noted)

Note : 1. Mounted on a FR4 PCB, single-sided copper, with 100cm² copper pad area

- 2.The testing condition of the thermal resistance (junction to lead) is based on 10 mm lead length between two 10cm x 10cm FR4 PCB single-sided copper pad
- 3. Mounted on a FR4 PCB, single-sided copper, mini pad
- 4. Reverse Recovery Test Conditions : IF=0.5A, IR=1A, Recover to 0.25A



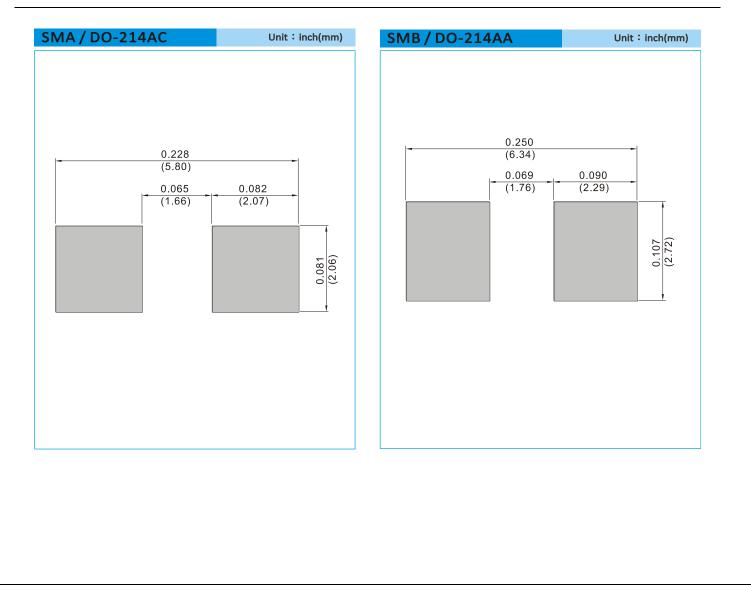


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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
MUR160K_AY_00001	DO-41	5K pcs/ TB 52mm	MUR160K	Halogen free
MUR160K_AY_10001	DO-41	5K pcs/ TB 52mm	MUR160K	RoHS
MURA1J_R1_00001	SMA	1.8K pcs / 7" reel	MURA1J	Halogen free
MURB1J_R1_00001	SMB	0.5K pcs / 7" reel	MURB1J	Halogen free

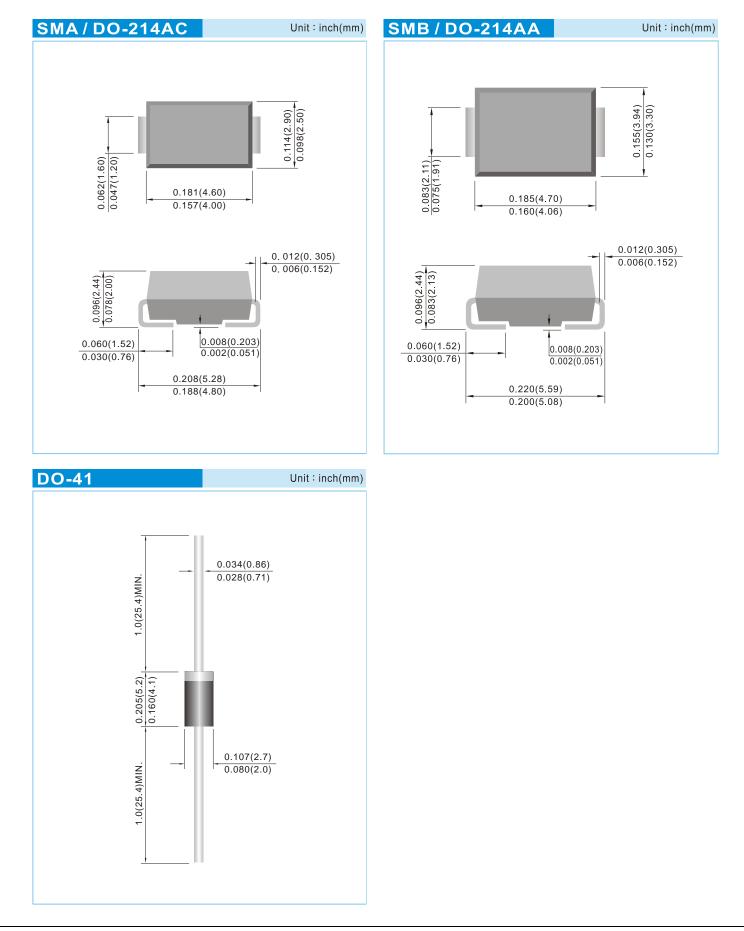
MOUNTING PAD LAYOUT







MURA1J/MURB1J/MUR160K







MURA1J / MURB1J / MUR160K

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