



# PJD70N06 / PJP70N06

## 60V N-Channel Enhancement Mode MOSFET

**Voltage**

**60 V**

**Current**

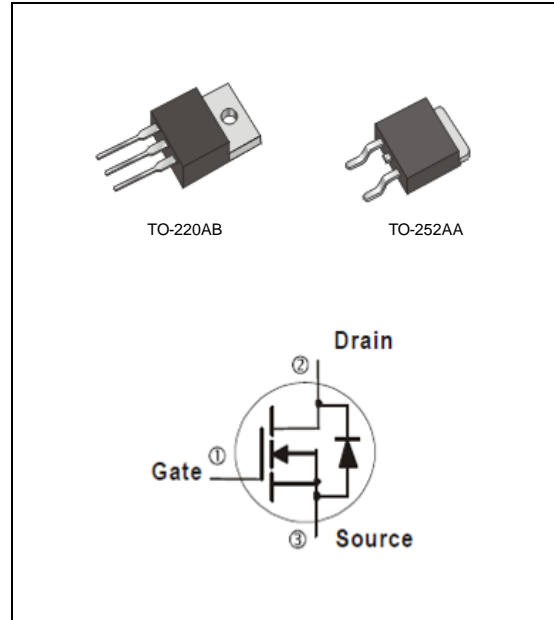
**70 A**

### Features

- $R_{DS(ON)}$ ,  $V_{GS}@10V, I_D@20A < 8.5m\Omega$
- High switching speed
- Low Gate Charge
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

### Mechanical Data

- Case: TO-252AA, TO-220AB Package
- Terminals: Solderable per MIL-STD-750, Method 2026



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	TO-220AB	TO-252AA	UNITS
Drain-Source Voltage		$V_{DS}$	60		V
Gate-Source Voltage		$V_{GS}$	$\pm 25$		V
Continuous Drain Current	$T_C=25^\circ\text{C}$	$I_D$	70		A
	$T_C=100^\circ\text{C}$		44		
Pulsed Drain Current		$I_{DM}$	180		
Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	100	83	W
	$T_C=100^\circ\text{C}$		40	33	
Single Pulse Avalanche Energy <sup>(Note 6)</sup>		$E_{AS}$	101		mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150		$^\circ\text{C}$
Typical Thermal Resistance					$^\circ\text{C}/\text{W}$
- Junction to Case		$R_{\theta JC}$	1.25	1.5	
- Junction to Ambient		$R_{\theta JA}$	62.5	110	

- Limited only By Maximum Junction Temperature



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### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2.0	3.0	4.0	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	7	8.5	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V, V <sub>GS</sub> =0V	-	-	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> (Note 7)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V (Note 2,3)	-	71	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	16	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	26	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, f=1.0MHZ	-	3116	-	pF
Output Capacitance	C <sub>oss</sub>		-	317	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	214	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =30V, I <sub>D</sub> =30A, V <sub>GS</sub> =10V, R <sub>G</sub> =25Ω (Note 2,3)	-	20	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	100	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	66	-	
Turn-Off Fall Time	t <sub>f</sub>		-	84	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	70	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A, V <sub>GS</sub> =0V	-	0.8	1.3	V

**NOTES :**

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> = 25°C.
4. The maximum current rating is package limited.
5. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. The test condition is L=0.1mH, I<sub>AS</sub>=45A, R<sub>G</sub>=25ohm, Starting T<sub>J</sub>=25°C
7. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

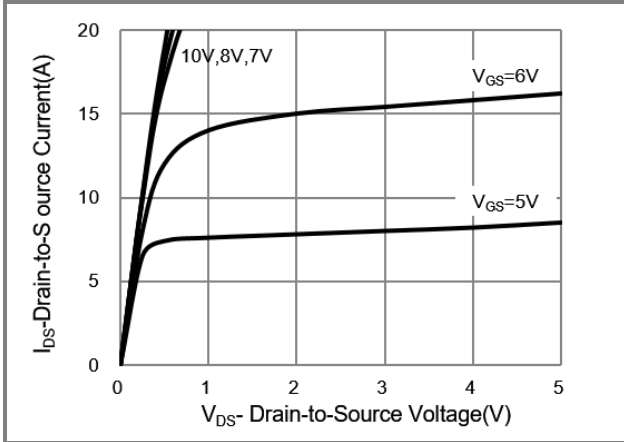


Fig.1 Output Characteristics

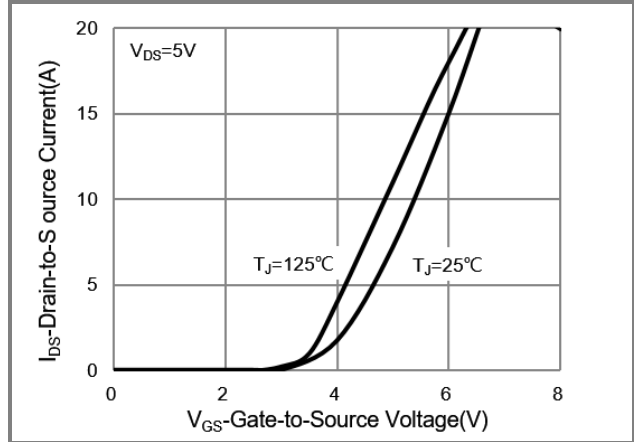


Fig.2 Transfer Characteristics

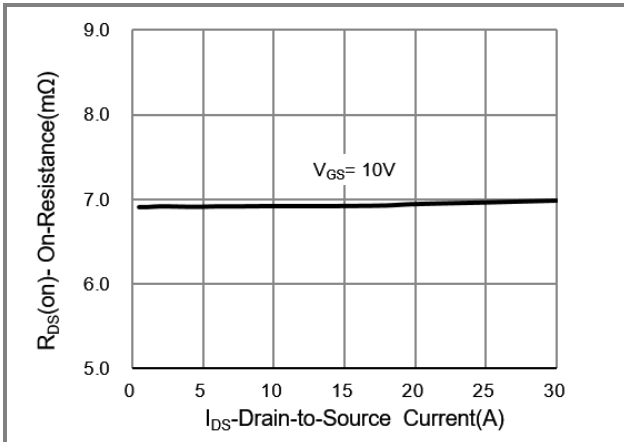


Fig.3 On-Resistance vs. Drain Current

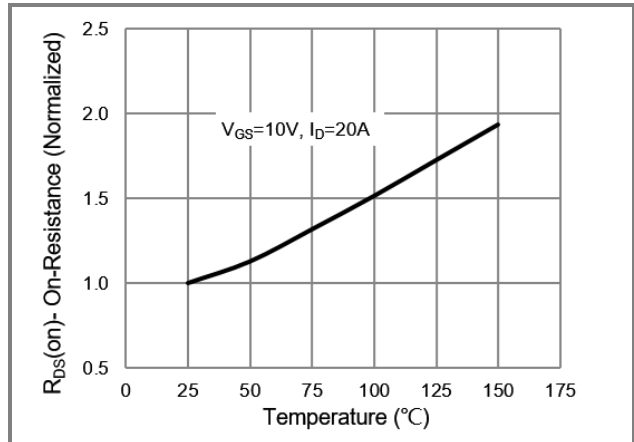


Fig.4 On-Resistance vs. Junction temperature

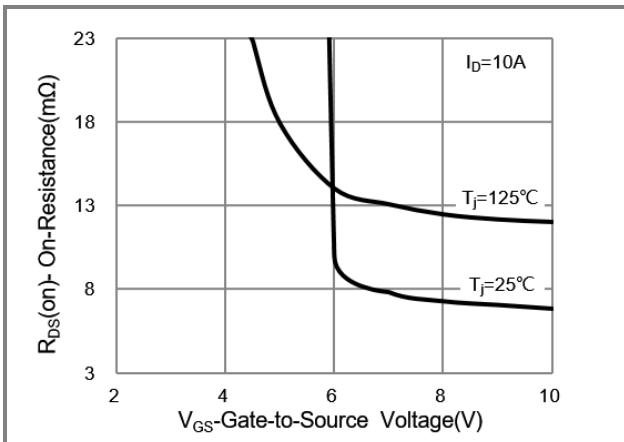


Fig.5 On-Resistance Variation with  $V_{GS}$ .

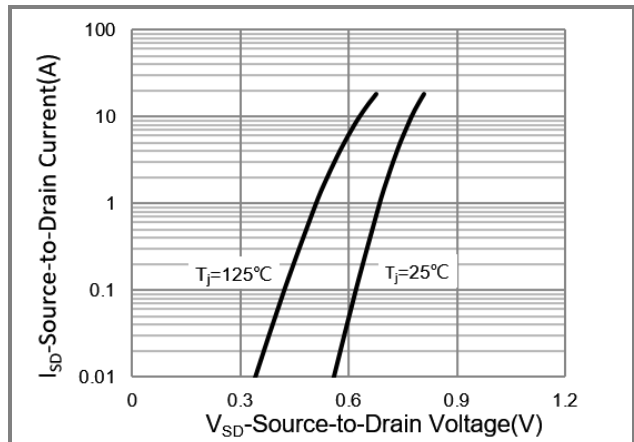


Fig.6 Source-Drain Diode Forward Voltage



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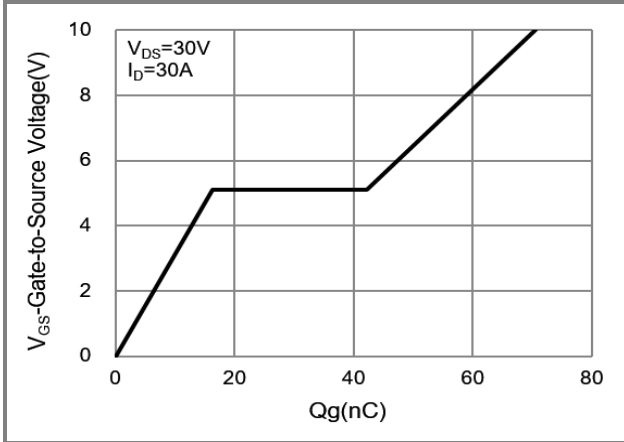


Fig.7 Gate-Charge Characteristics

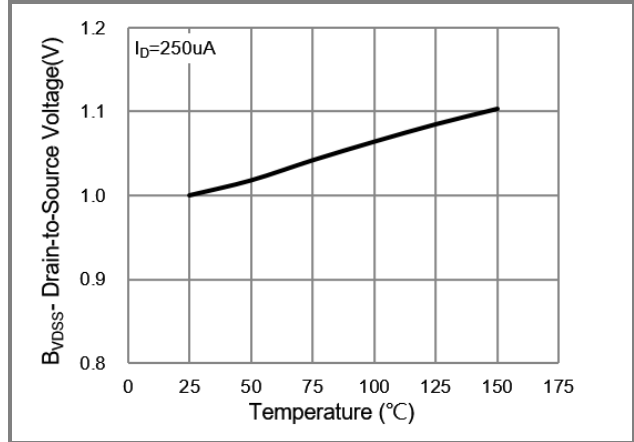


Fig.8 Breakdown Voltage Variation vs. Temperature

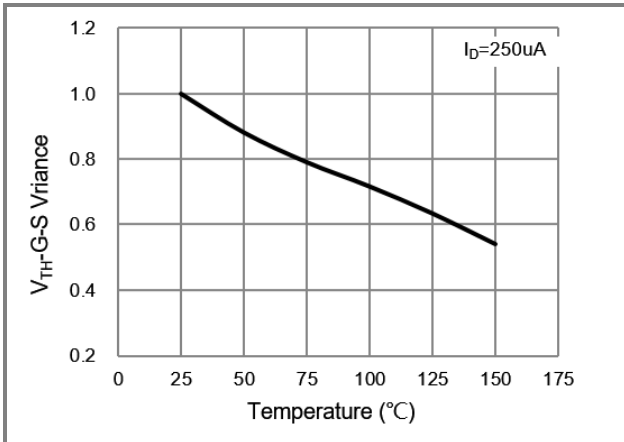


Fig.9 Threshold Voltage Variation with Temperature

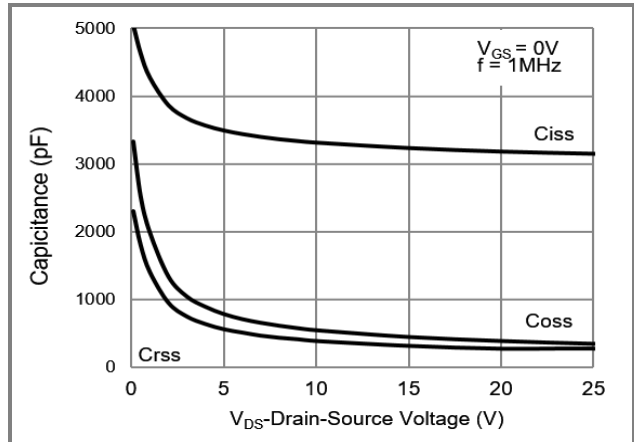


Fig.10 Capacitance vs. Drain-Source Voltage

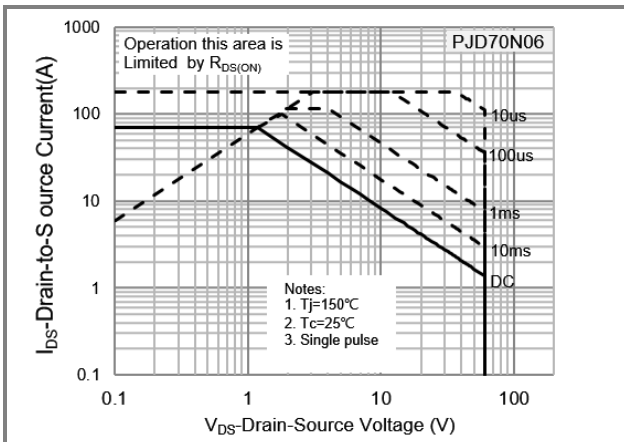


Fig.11 Maximum Safe Operating Area

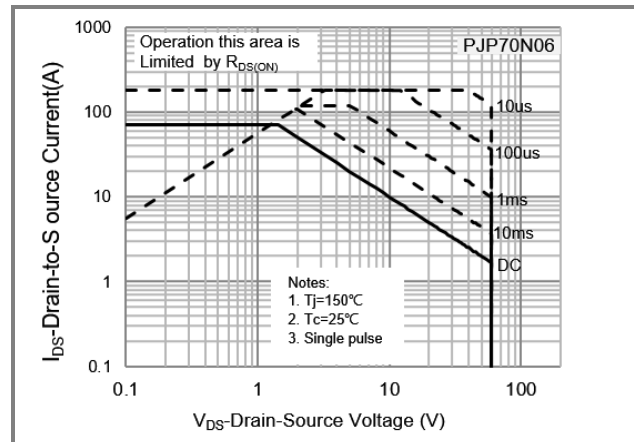


Fig.12 Maximum Safe Operating Area



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## TYPICAL CHARACTERISTIC CURVES

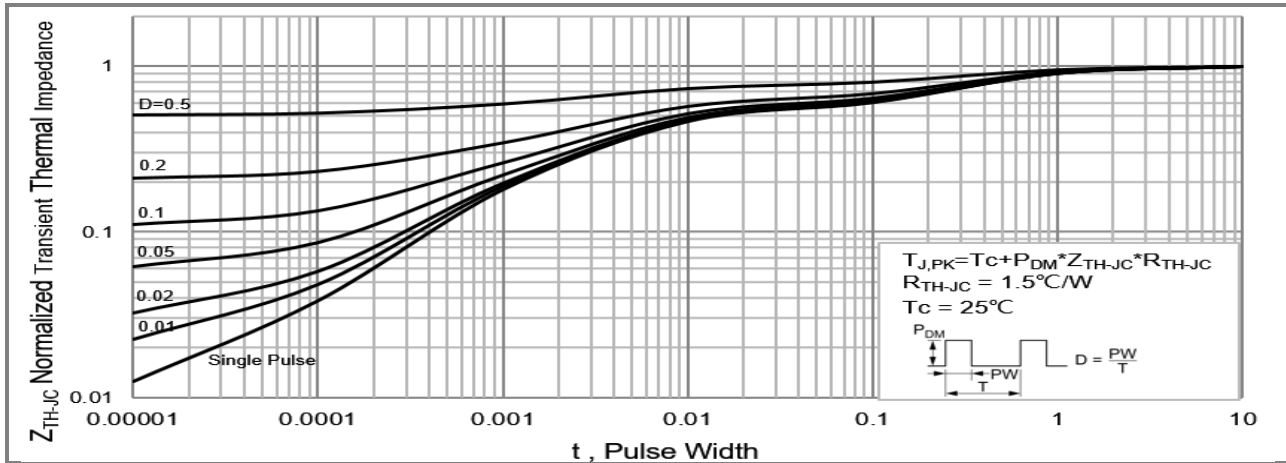


Fig.13 PJU/PJD Normalized Transient Thermal Impedance vs. Pulse Width

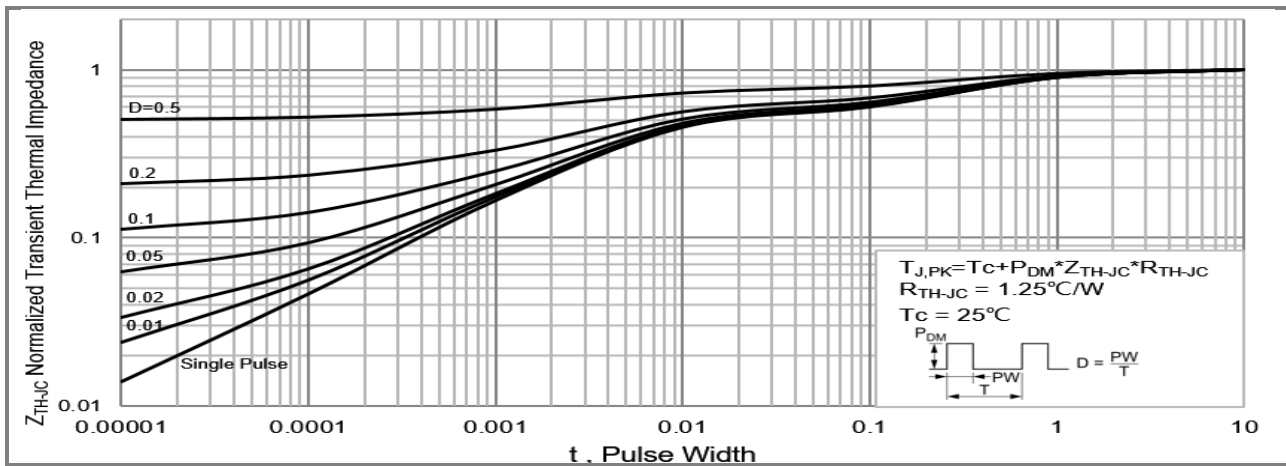
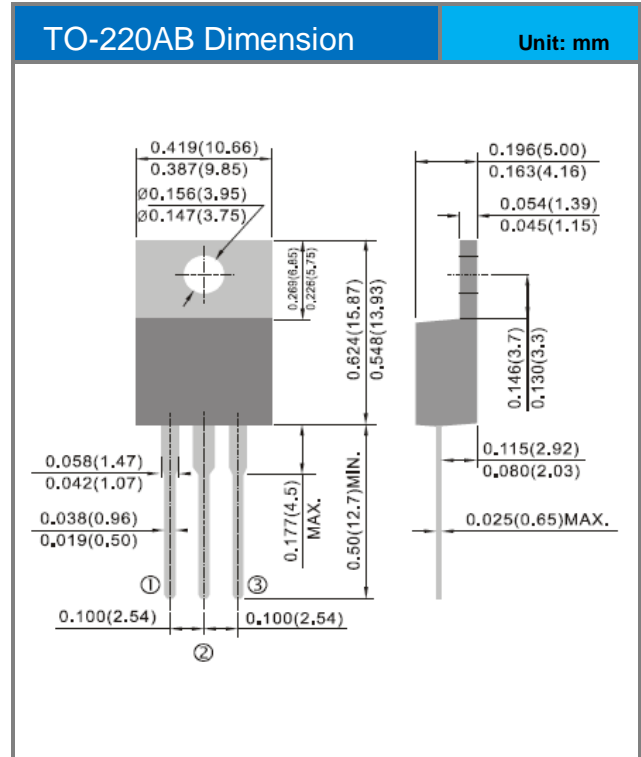
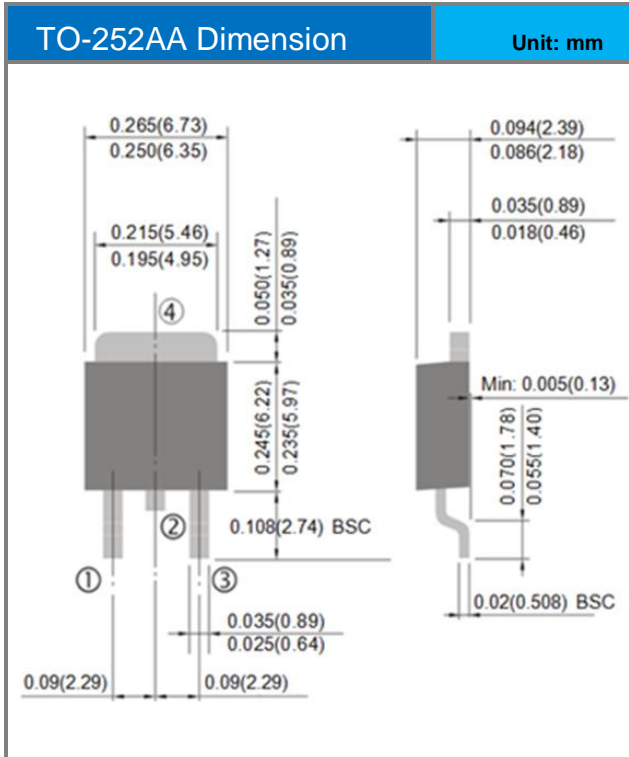


Fig.14 Normalized Transient Thermal Impedance vs. Pulse Width



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## Packaging Information



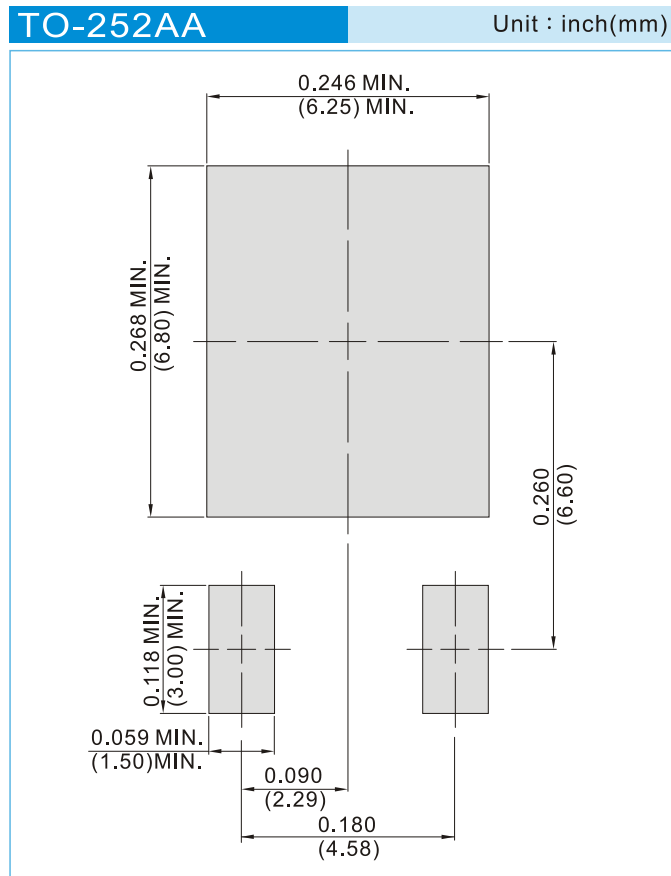


# PJD70N06 / PJP70N06

## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJD70N06_L2_00001	TO-252AA	3,000pcs / 13" reel	D70N06	Halogen free
PJP70N06_TO_00001	TO-220AB	50pcs / Tube	P70N06	Halogen free

## MOUNTING PAD LAYOUT





## PJD70N06 / PJP70N06

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