



60V N-Channel Enhancement Mode MOSFET

Voltage

60 V

Current

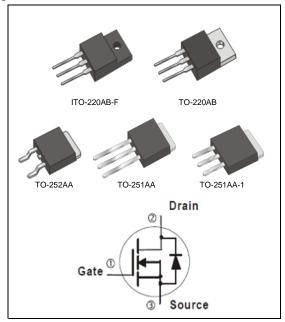
45 A

Features

- R_{DS(ON)}, V_{GS}@10V,I_D@20A<12mΩ
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_{D}@15A<15m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive).
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

- Case: TO-251AA, TO-251AA-1, TO-252AA, TO-220AB, ITO-220AB-F Package
- Terminals: Solderable per MIL-STD-750, Method 2026



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	TO-251AA TO-251AA-1	TO-220AB	ITO-220AB-F	TO-252AA	UNITS
Drain-Source Voltage		V _{DS}	60				V
Gate-Source Voltage		V_{GS}	<u>+</u> 20				V
Continuous Drain Current	T _C =25°C	- I _D	45	55	35	45	A
	T _C =100°C		29	35	22	29	
Pulsed Drain Current	T _C =25°C	I _{DM}	180	220	140	180	
Power Dissipation	T _C =25°C	PD	63	96	42	63	W
	T _C =100°C		25	38	17	25	
Single Pulse Avalanche Energy (Note 6)		E _{AS}	61				mJ
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150				°C
Typical Thermal Resistance							
Junction to CaseJunction to Ambient		$R_{ heta JC} \ R_{ heta JA}$	2.0 110	1.3 62.5	3 120	2.0 110	°C/W

Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =250uA	60	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.0	1.7	2.5	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V,I _D =20A	-	10.5	12		
		V _{GS} =4.5V,I _D =15A	-	12	15	mΩ	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =60V, V_{GS} =0V	-	0.01	1.0	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA	
Dynamic (Note 7)							
Total Gate Charge	Qg	V _{DS} =30V, I _D =10A, V _{GS} =10V (Note 1,2)	-	39	-	nC	
Gate-Source Charge	Q_{gs}		-	6.1	-		
Gate-Drain Charge	Q_{gd}		-	6.7	-		
Input Capacitance	Ciss), or, , , , , , , , , , , , , , , , , ,	-	2256	-	pF	
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, f=1.0MHZ	-	145	-		
Reverse Transfer Capacitance	Crss	I=1.UIVIMZ	-	93	-		
Turn-On Delay Time	td _(on)	\/ 45\/ L 40A	-	7.5	-	ns	
Turn-On Rise Time	t _r	V _{DD} =15V, I _D =10A,	-	36	-		
Turn-Off Delay Time	td _(off)	$V_{GS}=10V, R_G=6\Omega$ (Note 1,2)	-	49	-		
Turn-Off Fall Time	t _f		-	12	-		
Drain-Source Diode							
Maximum Continuous Drain-Source			1	-	45	А	
Diode Forward Current	I _S						
Diode Forward Voltage	V_{SD}	I _S =1A,V _{GS} =0V	-	0.67	1.0	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited.
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, I_{AS} =35A, V_{DD} =25V, V_{GS} =10V
- 7. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

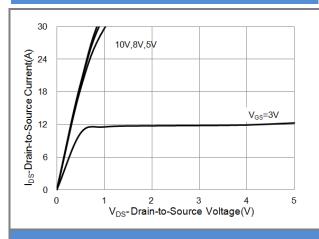


Fig.1 Output Characteristics

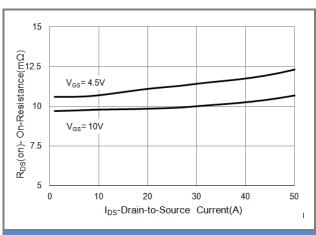


Fig.3 On-Resistance vs. Drain Current

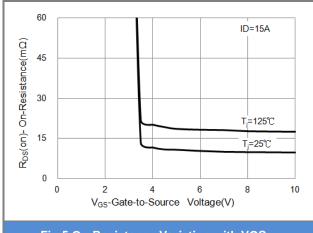


Fig.5 On-Resistance Variation with VGS.

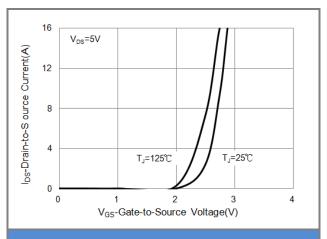


Fig.2 Transfer Characteristics

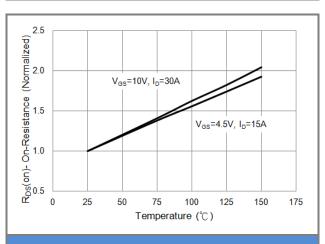


Fig.4 On-Resistance vs. Junction temperature

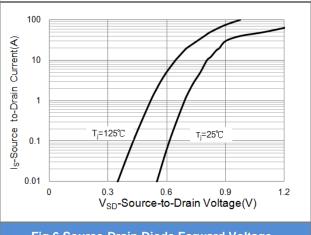


Fig.6 Source-Drain Diode Forward Voltage





TYPICAL CHARACTERISTIC CURVES

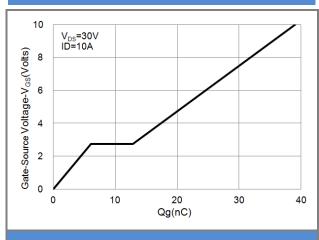


Fig.7 Gate-Charge Characteristics

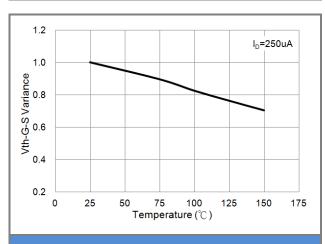
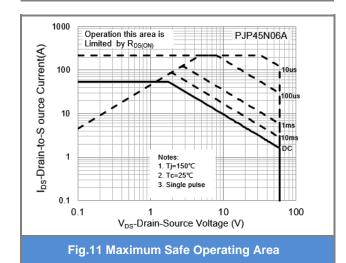


Fig.9 Threshold Voltage Variation with Temperature



10000 Ciss

1000

Vos = 0V

f = 1MHz

10

V_{DS}-Drain-Source Voltage (V)

Fig.8 Capacitance vs. Drain-Source Voltage

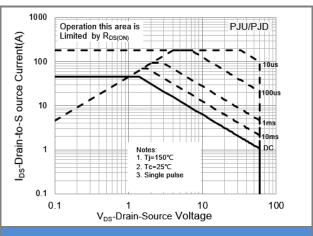
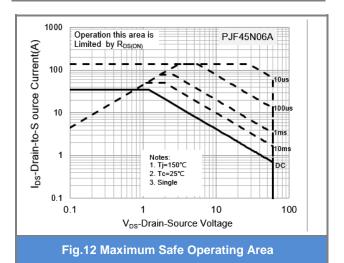


Fig.10 Maximum Safe Operating Area







TYPICAL CHARACTERISTIC CURVES

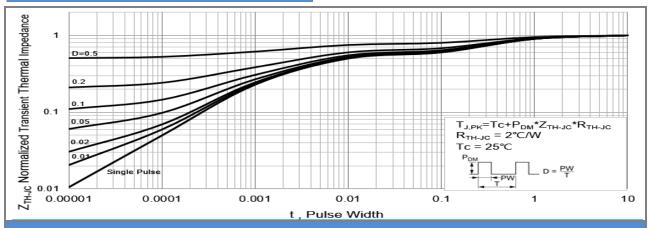


Fig.13 PJU/PJD Normalized Transient Thermal Impedance vs. Pulse Width

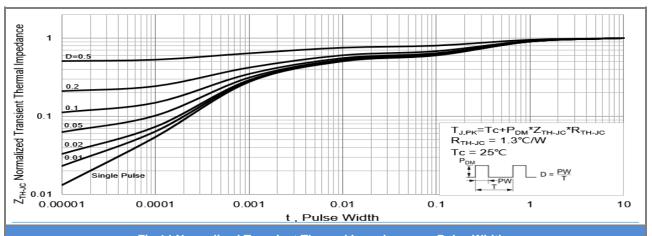


Fig.14 Normalized Transient Thermal Impedance vs. Pulse Width

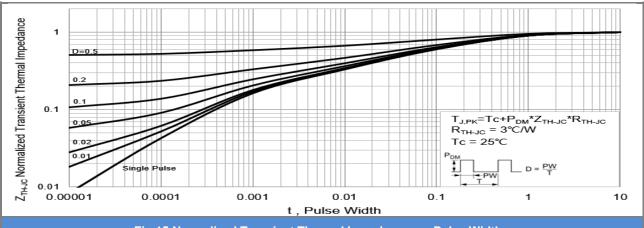
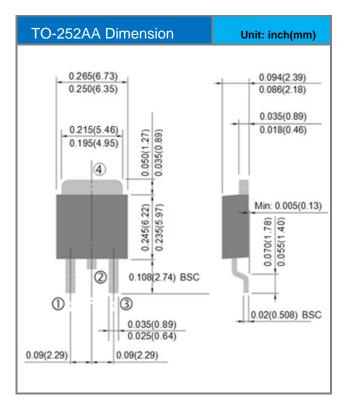


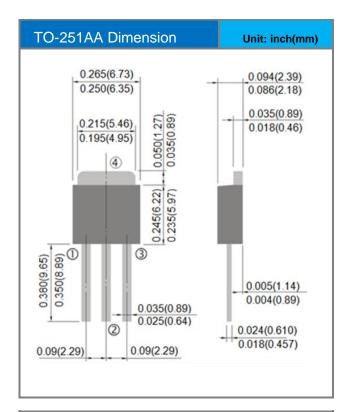
Fig.15 Normalized Transient Thermal Impedance vs. Pulse Width

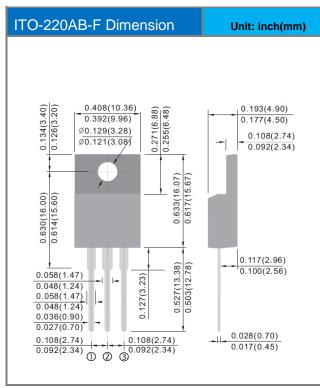


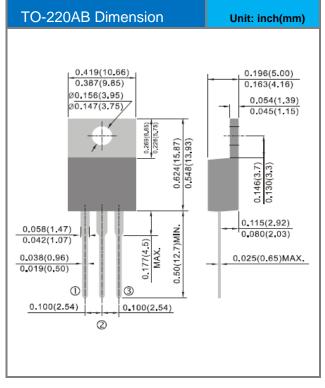


Packaging Information





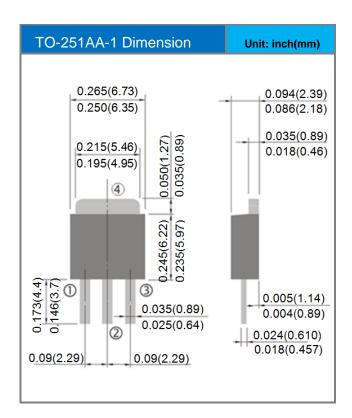








Packaging Information



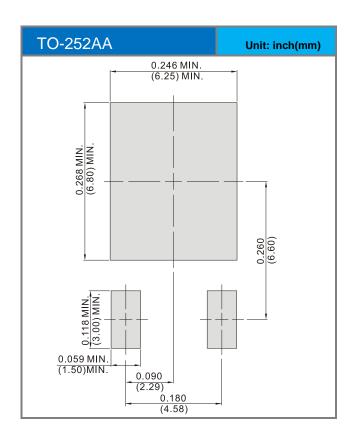




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJU45N06A_T0_00001	TO-251AA	80pcs / Tube	U45N06A	Halogen free
PJU45N06A-1_T0_00001	TO-251AA-1	80pcs / Tube	45N06A	Halogen free
PJD45N06A_L2_00001	TO-252AA	3,000pcs / 13" reel	D45N06A	Halogen free
PJP45N06A_T0_00001	TO-220AB	50pcs / Tube	P45N06A	Halogen free
PJF45N06A_T0_00001	ITO-220AB-F	50pcs / Tube	F45N06A	Halogen free

MOUNTING PAD LAYOUT







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