



60V P-Channel Enhancement Mode MOSFET

Voltage

-60 V

Current

-15 A

Features

- $R_{DS(ON)}$, V_{GS} @-10V, I_{D} @-7.5A<68m Ω
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_D@-4.0A<85m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case: DFN5060-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0028 ounces, 0.08 grams
- Marking: Q5463A

DFN5060-8L

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETE	२	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V_{DS}	-60	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Comment	T _C =25°C	I _D	-15	А	
Continuous Drain Current	T _C =100°C		-9.5		
Pulsed Drain Current (Note 1)	T _C =25°C	I_{DM}	-60		
Power Dissipation	T _C =25°C	Po	25	10/	
	T _C =100°C		10	W	
Continuous Drain Current	T _A =25°C	I _D	-4.0	Α	
	T _A =70°C		-3.2	Α	
Power Dissipation	T _A =25°C	ć	2.0	W	
Power Dissipation	T _A =70°C	Pb	1.3		
Single Pulse Avalanche Energy (Note 6)		E _{AS}	31	mJ	
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55~150	°C	
T : 171 15 : (Note 4.5)	Junction to Case	$R_{ heta JC}$	5.0	°C/W	
Typical Thermal Resistance (Note 4,5)	Junction to Ambient	$R_{\theta JA}$	62.5		

• Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-60	-	1	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1.0	-1.63	-2.5	V	
Duein Course On Chata Basistana	R _{DS(on)}	V _{GS} =-10V,I _D =-7.5A	-	55	68	mΩ	
Drain-Source On-State Resistance		V _{GS} =-4.5V,I _D =-4.0A	-	73	85		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-60V, V_{GS} =0V	-	-	-1.0	uA	
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA	
Dynamic (Note 7)							
Total Gate Charge	Q_g	V_{DS} =-30V, I_{D} =-7.5A, V_{GS} =-10V (Note 3)	-	17	-	nC	
Gate-Source Charge	Q_{gs}		-	2.8	-		
Gate-Drain Charge	Q_{gd}		-	3.6	-		
Input Capacitance	Ciss	V _{DS} =-30V, V _{GS} =0V, f=1.0MHZ	-	879	-	pF	
Output Capacitance	Coss		-	70	-		
Reverse Transfer Capacitance	Crss	I=1.UIVINZ	-	47	-		
Turn-On Delay Time	td _(on)	\/ 20\/ I 4A	-	8.4	-		
Turn-On Rise Time	t _r	V_{DD} =-30V, I_{D} =-1A, V_{GS} =-10V, R_{G} =6 Ω (Note 3)	-	30	-	ns	
Turn-Off Delay Time	td _(off)		-	52	ı		
Turn-Off Fall Time	t _f		-	16	ı		
Drain-Source Diode							
Maximum Continuous Drain-Source					-15	Α	
Diode Forward Current	I _S		-	-	-10	A	
Diode Forward Voltage	V_{SD}	I _S =1A,V _{GS} =0V	-	-0.73	-1.0	V	

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited.
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, I_{AS} =25A, V_{DD} =25V, V_{GS} =10V
- 7. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

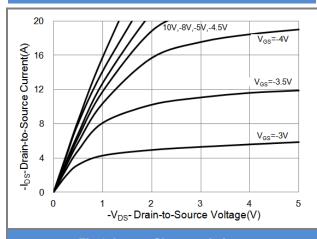


Fig.1 Output Characteristics

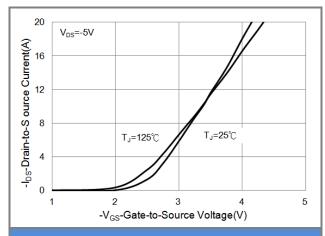


Fig.2 Transfer Characteristics

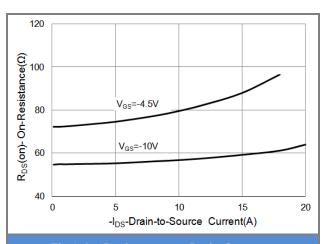


Fig.3 On-Resistance vs. Drain Current

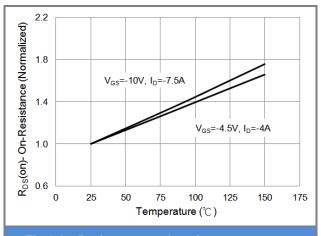


Fig.4 On-Resistance vs. Junction temperature

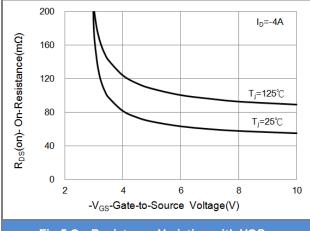


Fig.5 On-Resistance Variation with VGS.

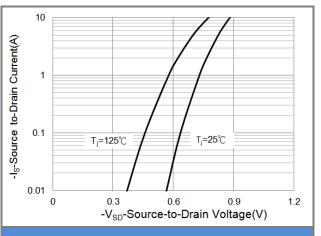


Fig.6 Source-Drain Diode Forward Voltage





TYPICAL CHARACTERISTIC CURVES

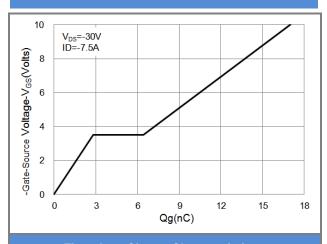


Fig.7 Gate-Charge Characteristics

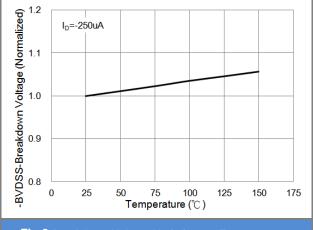


Fig.8 Breakdown Voltage Variation vs. Temperature

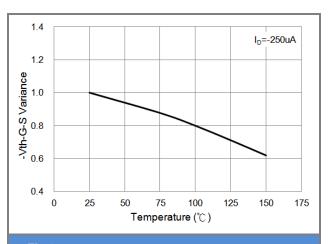


Fig.9 Threshold Voltage Variation with Temperature

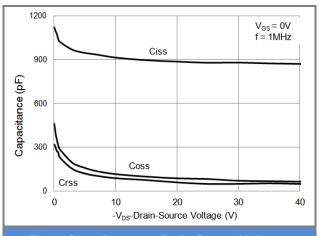


Fig.10 Capacitance vs. Drain-Source Voltage

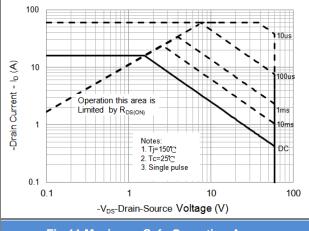


Fig.11 Maximum Safe Operating Area





TYPICAL CHARACTERISTIC CURVES

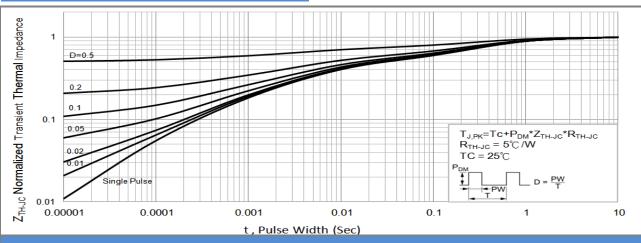


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

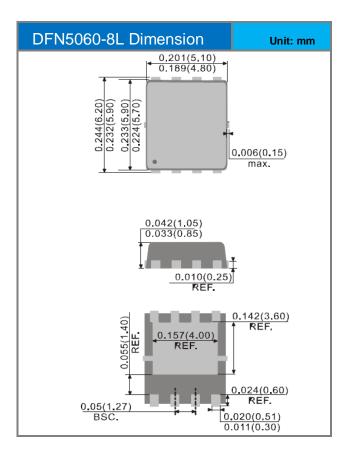


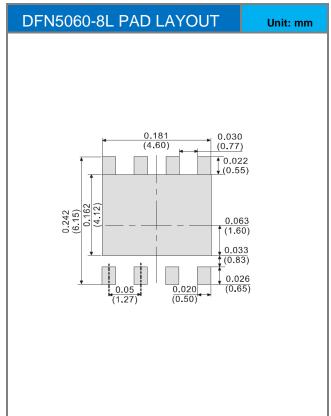


PART NO PACKING CODE VERSION

Part No Packing Code	g Code Package Type Pac		ng type Marking	
PJQ5463A_R2_00001	DFN5060-8L	3000pcs / 13" reel	Q5463A	Halogen free

Packaging Information & Mounting Pad Layout









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