



30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

-35 A

Features

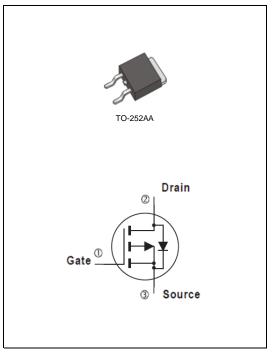
- $R_{DS(ON)}$, $V_{GS}@-10V$, $I_D@-8A<19m\Omega$
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_{D}@-5A<30m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

• Case: TO-252AA Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.0104 ounces, 0.297grams



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETE	ER .	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V_{DS}	-30	V	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _C =25°C	I _D	-35	А	
	T _C =100°C		-22		
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	-140		
Power Dissipation	T _C =25°C	Po	35	W	
	T _C =100°C		14		
Continuous Drain Current	T _A =25°C	I _D	-8.4	Α	
	T _A =70°C		-6.7	Α	
Power Dissipation	T _A =25°C	5	2.0	W	
Power Dissipation	T _A =70°C	Pb	1.3		
Operating Junction and Storage	Temperature Range	T_{J} , T_{STG}	-55~150	°C	
(Note 4.5)	Junction to Case	$R_{ heta JC}$	3.6	°C/W	
Typical Thermal Resistance ^(Note 4,5)	Junction to Ambient	$R_{\theta JA}$	62.5		

• Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V,I _D =-250uA	-30	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1	-1.5	-2.5	V	
Danier Course On Otata Basistana	R _{DS(on)}	V _{GS} =-10V,I _D =-8A	-	15	19	mΩ	
Drain-Source On-State Resistance		V_{GS} =-4.5V, I_{D} =-5A	-	24	30		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-	-1.0	uA	
Gate-Source Leakage Current	I_{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	ı	-	<u>+</u> 100	nA	
Dynamic (Note 6)							
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-5A, V _{GS} =-4.5V ^(Note 1,2)	-	11	-	nC	
Gate-Source Charge	Q_gs		-	3.2	-		
Gate-Drain Charge	Q_gd		-	3.9	-		
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V,	-	1169	-	pF	
Output Capacitance	Coss		-	180	-		
Reverse Transfer Capacitance	Crss	I=1.0IVINZ	-	132	-		
Turn-On Delay Time	td _(on)	V_{DS} =-15V,ID=-1A, V_{GS} =-10V, R_{G} =6 Ω (Note 1,2)	-	5.9	-		
Turn-On Rise Time	t _r		-	33	-	ns	
Turn-Off Delay Time	td _(off)		-	55	-		
Turn-Off Fall Time	t _f		-	34	-		
Drain-Source Diode							
Maximum Continuous Drain-Source	ı		-	-	-35	А	
Diode Forward Current	I _S						
Reverse Recovery Time	V_{SD}	I _S =-1A,V _{GS} =0V	-	-0.73	-1.0	V	

NOTES:

- 1. Pulse width < 300us, Duty cycle < 2%
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited
- 5. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

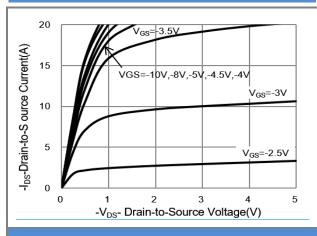


Fig.1 On-Region Characteristics

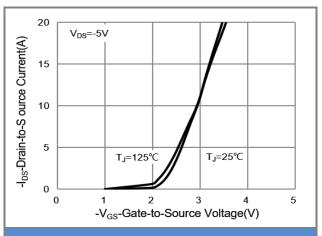


Fig.2 Transfer Characteristics

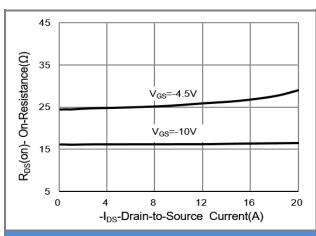


Fig.3 On-Resistance vs. Drain Current

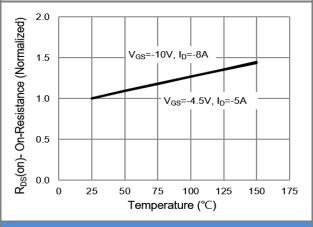


Fig.4 On-Resistance vs. Junction temperature

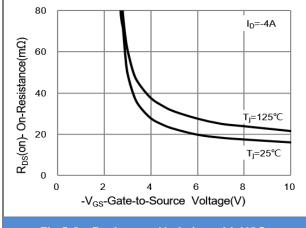


Fig.5 On-Resistance Variation with VGS.

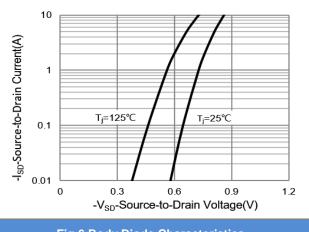


Fig.6 Body Diode Characteristics





TYPICAL CHARACTERISTIC CURVES

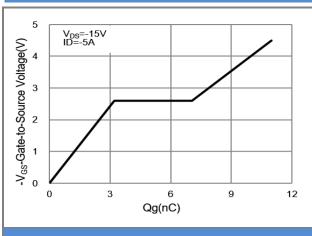
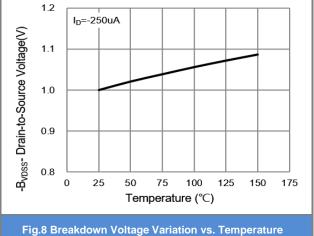


Fig.7 Gate-Charge Characteristics



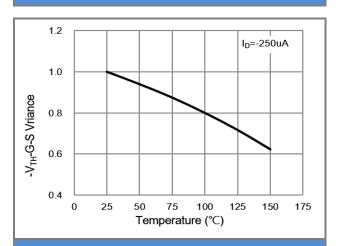


Fig.9 Threshold Voltage Variation with Temperature.

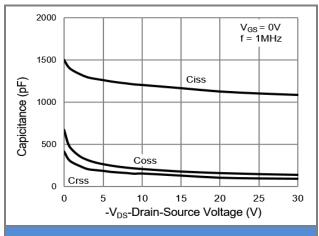


Fig.10 Capacitance vs. Drain-Source Voltage.

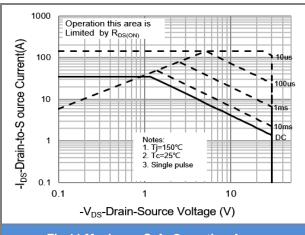


Fig.11 Maximum Safe Operating Area





TYPICAL CHARACTERISTIC CURVES

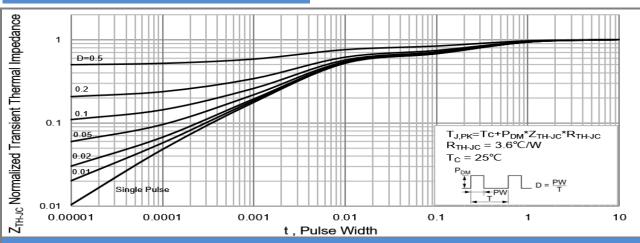
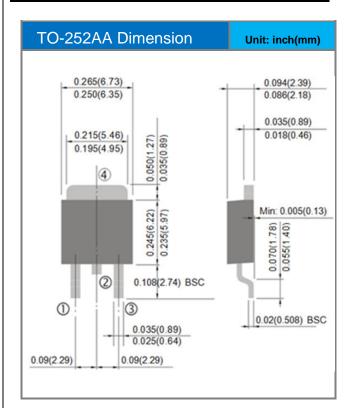


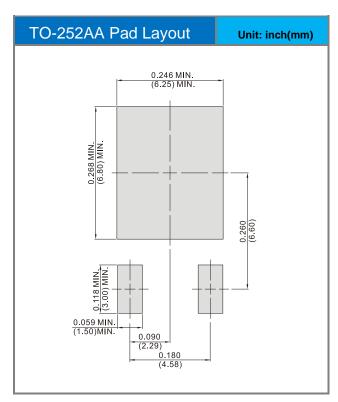
Fig.12 Normalized Thermal Transient Impedance





Packaging Information









PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version	
PJD35P03_L2_00001	TO-252AA	3,000pcs / 13" reel	D35P03	Halogen free	





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