



### 30V Dual P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

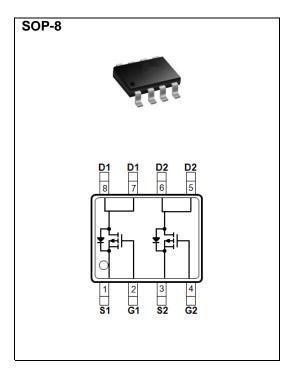
-4 A

#### **Features**

- $R_{DS(ON)}$ ,  $V_{GS}$ @-10V,  $I_{D}$ @-3A<52m $\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V$ ,  $I_{D}@-2A<82m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0029 ounces, 0.083 grams



### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		$V_{DS}$	-30	V	
Gate-Source Voltage		$V_{GS}$	<u>+</u> 20	V	
Continuous Drain Current	T <sub>A</sub> =25°C		-4	А	
	T <sub>A</sub> =70°C	I <sub>D</sub>	-3		
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	-16	А	
Power Dissipation	T <sub>A</sub> =25°C		1.7	W	
	T <sub>A</sub> =70°C	P <sub>D</sub>	1.1		
Operating Junction and Storage Temperature Range		$T_{J}$ , $T_{STG}$	-55~150	°C	
Typical Thermal Resistance					
- Junction to Ambient (Note 5)		$R_{\theta JA}$	73.5	°C/W	





### **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250uA$	-1	-1.6	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A	-	42	52	mΩ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	62	82	mΩ
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)			_			
Total Gate Charge	$Q_g$	V <sub>DS</sub> =-15V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	4.8	-	nC
Gate-Source Charge	$Q_gs$		-	1.7	-	
Gate-Drain Charge	$Q_gd$		-	1.7	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1.0MHZ	-	516	-	pF
Output Capacitance	Coss		-	83	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DS}$ =-15V, $I_{D}$ =-1A, $V_{GEN}$ =-10V, $R_{G}$ =6 $\Omega$ (Note 1,2)	-	5.6	-	ns
Turn-On Rise Time	tr		-	8.5	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	27	-	
Turn-Off Fall Time	tf		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	-4	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.75	-1	V

#### NOTES:

- 1. Pulse width<a>300us</a>, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 5. R<sub>OJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





#### **TYPICAL CHARACTERISTIC CURVES**

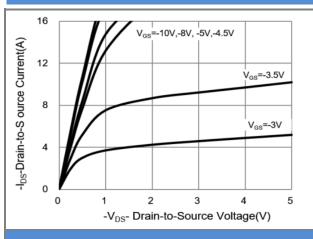
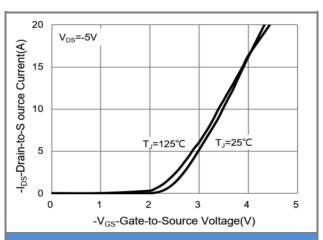


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

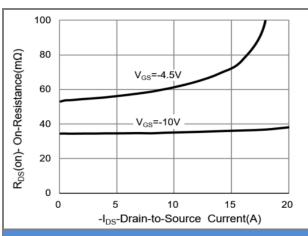


Fig.3 On-Resistance vs. Drain Current

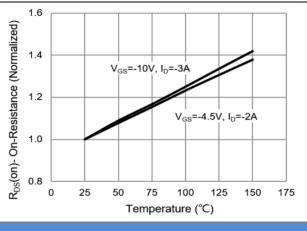


Fig.4 On-Resistance vs. Junction temperature

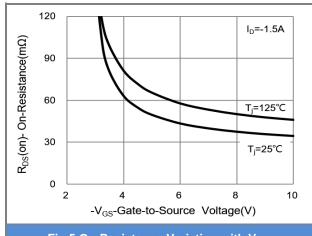
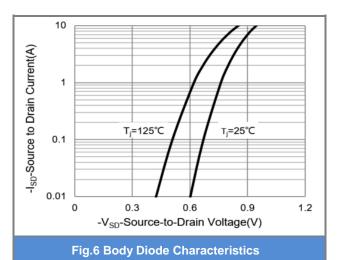


Fig.5 On-Resistance Variation with  $V_{\text{GS}}$ 







#### **TYPICAL CHARACTERISTIC CURVES**

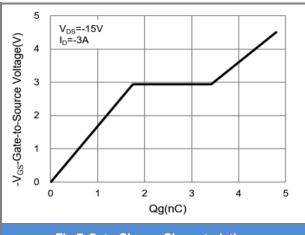


Fig.7 Gate-Charge Characteristics

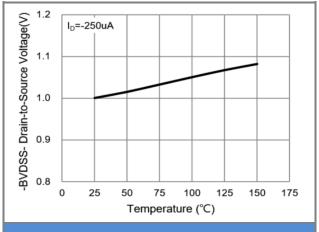


Fig.8 Breakdown Voltage Variation vs. Temperature

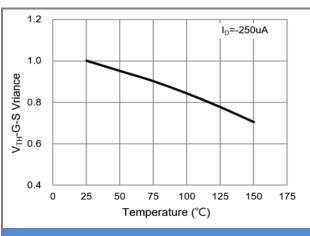


Fig.9 Threshold Voltage Variation with Temperature

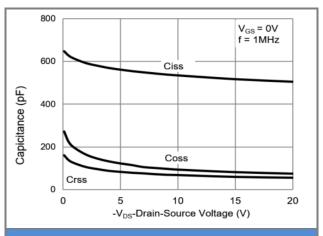


Fig.10 Capacitance vs. Drain-Source Voltage

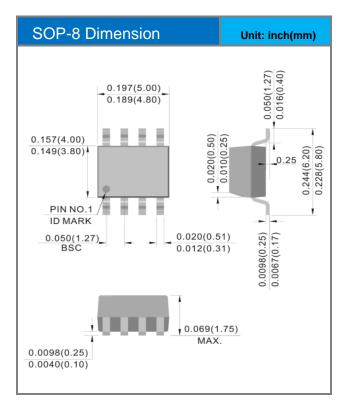


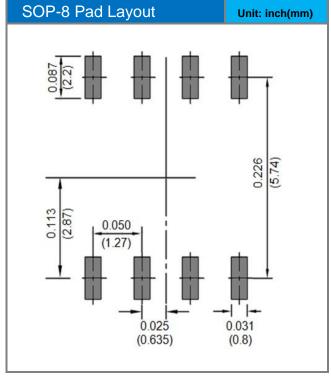


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9807_R2_00001	SOP-8	2.5K pcs / 13" reel	L9807	Halogen free

### **Packaging Information & Mounting Pad Layout**









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