



### 30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

-4.6 A

#### **Features**

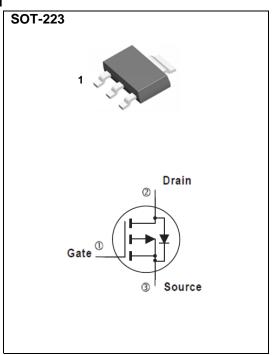
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@-10V, I<sub>D</sub>@-3A<50mΩ
- R<sub>DS(ON)</sub>, V<sub>GS</sub>@-4.5V, I<sub>D</sub>@-2A<80mΩ
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

• Case: SOT-223 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• SOT-223 Approx. Weight: 0.043 ounces, 0.123grams



## $\textbf{Maximum Ratings and Thermal Characteristics} \; (T_A = 25 ^{\circ} \text{C unless otherwise noted})$

PARAMETI	ER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage Gate-Source Voltage		V <sub>DS</sub>	-30		
		$V_{GS}$	<u>+</u> 20	V	
Continuous Drain Current	T <sub>A</sub> =25°C	- I <sub>D</sub>	-4.6		
	T <sub>A</sub> =70°C		-3.7	А	
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	-20		
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	1.6		
	T <sub>A</sub> =70°C		1.0	W	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient (Note 5)		$R_{\theta JA}$	78.1	°C/W	

• Limited only By Maximum Junction Temperature





### **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS		
Static								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	-	-	- V		
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250uA$	-1	-1.6	-2.5			
Drain Source On State Resistance	D	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A	-	40	50	mO.		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	60	80	mΩ		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	uA		
Gate-Source Leakage Current	$I_{GSS}$	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA		
Dynamic (Note 6)								
Total Gate Charge	$Q_g$	V <sub>DS</sub> =-15V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	4.8	-	nC		
Gate-Source Charge	$Q_gs$		-	1.7	-			
Gate-Drain Charge	$Q_gd$		-	1.7	-			
Input Capacitance	Ciss	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1.0MHZ	-	516	-	pF		
Output Capacitance	Coss		-	83	-			
Reverse Transfer Capacitance	Crss	I=1.0IVII1Z	-	61	-			
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DS}$ =-15V, $I_{D}$ =-1A, $V_{GEN}$ =-10V, $R_{G}$ =6 $\Omega$ (Note 1,2)	-	5.6	-	ns		
Turn-On Rise Time	t <sub>r</sub>		-	8.5	-			
Turn-Off Delay Time	td <sub>(off)</sub>		-	27	-			
Turn-Off Fall Time	t <sub>f</sub>		-	18	-			
Drain-Source Diode								
Maximum Continuous Drain-Source	I.		-	-	-4.6	А		
Diode Forward Current	I <sub>S</sub>							
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.76	-1	V		

#### NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 5. R@JA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.





#### **TYPICAL CHARACTERISTIC CURVES**

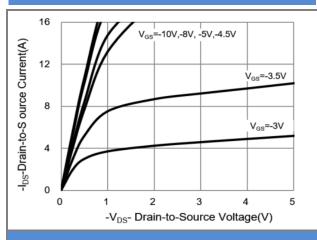
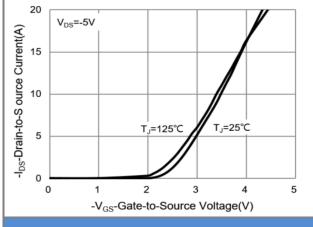


Fig.1 Output Characteristics



**Fig.2 Transfer Characteristics** 

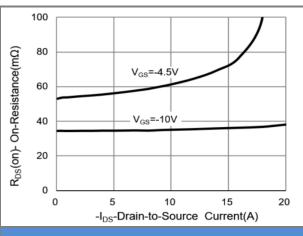


Fig.3 On-Resistance vs. Drain Current

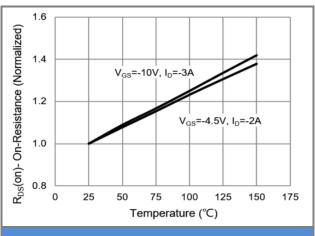


Fig.4 On-Resistance vs. Junction temperature

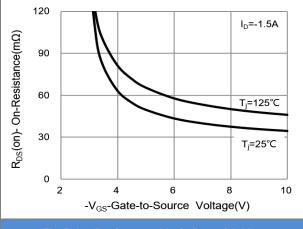


Fig.5 On-Resistance Variation with V<sub>GS</sub>

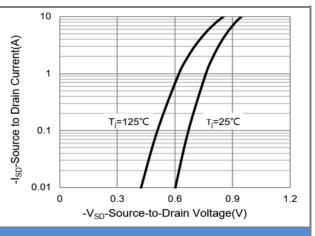


Fig.6 Source-Drain Diode Forward Voltage





#### **TYPICAL CHARACTERISTIC CURVES**

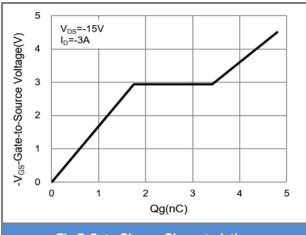


Fig.7 Gate-Charge Characteristics

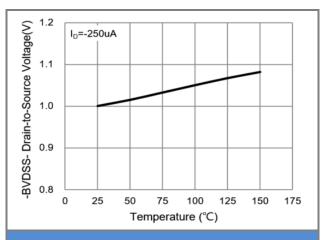


Fig.8 Breakdown Voltage Variation vs. Temperature

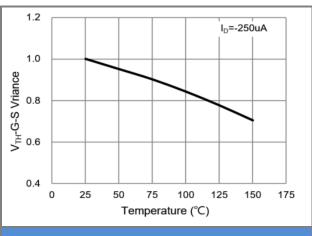


Fig.9 Threshold Voltage Variation with Temperature

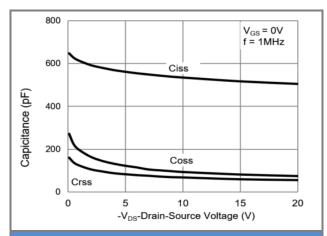
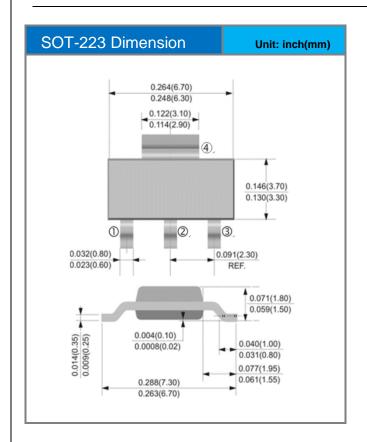


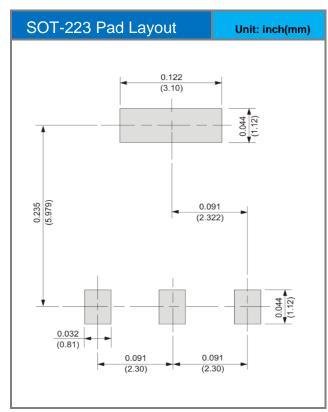
Fig.10 Capacitance vs. Drain-Source Voltage





### **Packaging Information & Mounting Pad Layout**









### **Part No Packing Code Version**

Part No Packing Code	Package Type Packing Type		Marking	Version	
PJW5P03_R2_00001	SOT-223	2,500pcs / 13" reel	W5P03	Halogen free	





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