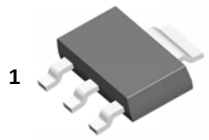
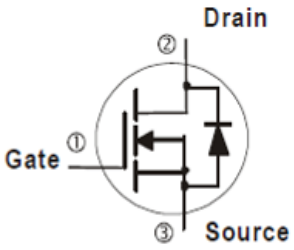
PJW8N03

30V N-Channel Enhancement Mode MOSFET

Voltage	30 V	Current	7.2 A

SOT-223





Features

- $R_{DS(ON)}$, V_{GS}@10V, I_D@5.6A<38mΩ
- $R_{DS(ON)}$, V_{GS}@4.5V, I_D@3.5A<55mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

- Case: SOT-223 Package
- Terminals: Solderable per MIL-STD-750, Method 2026

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C=25°C	I_D	7.2	A
	T_C=100°C		4.6	
Pulsed Drain Current		I_{DM}	28.8	A
Power Dissipation	T_C=25°C	P_D	3.0	W
	T_C=100°C		1.2	
Continuous Drain Current	T_A=25°C	I_D	5.0	A
	T_A=70°C		4.0	
Power Dissipation	T_A=25°C	P_D	1.5	W
	T_A=70°C		0.94	
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal Resistance - (Note 3)	Junction to Case	$R_{\theta JC}$	41.6	°C/W
	Junction to Ambient	$R_{\theta JA}$	85	

PJW8N03

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1.0	1.33	2.1	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V, I_D=5.6A	-	30	38	mΩ
		V_{GS}=4.5V, I_D=3.5A	-	42	55	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V, V_{GS}=0V	-	0.01	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	±10	±100	nA
Dynamic						
Total Gate Charge	Q_g	V_{DS}=15V, I_D=5.6A, V_{GS}=10V (Note 1,2)	-	7.8	-	nC
Gate-Source Charge	Q_{gs}		-	1.2	-	
Gate-Drain Charge	Q_{gd}		-	1.5	-	
Input Capacitance	Ciss	V_{DS}=15V, V_{GS}=0V, f=1.0MHZ	-	343	-	pF
Output Capacitance	Coss		-	48	-	
Reverse Transfer Capacitance	Crss		-	34	-	
Switching						
Turn-On Delay Time	$td_{(on)}$	V_{DD}=15V, I_D=5.6A, V_{GS}=10V, R_G=3Ω (Note 1,2)	-	3	-	ns
Turn-On Rise Time	tr		-	40	-	
Turn-Off Delay Time	$td_{(off)}$		-	38	-	
Turn-Off Fall Time	tf		-	39	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	1.5	A
Diode Forward Voltage	V_{SD}	I_S=1.0A, V_{GS}=0V	-	0.77	1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited

PJW8N03

TYPICAL CHARACTERISTIC CURVES

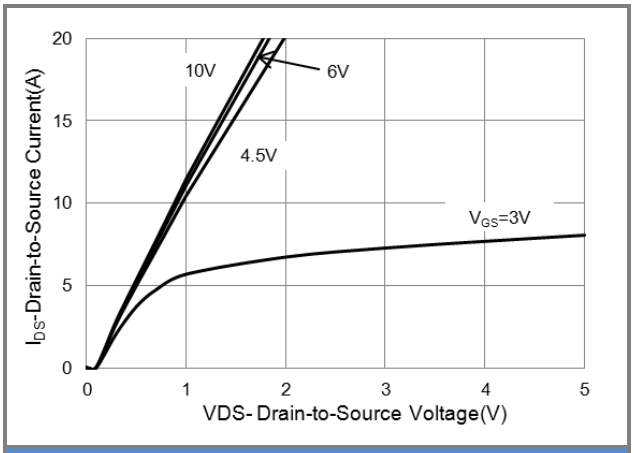


Fig.1 On-Region Characteristics

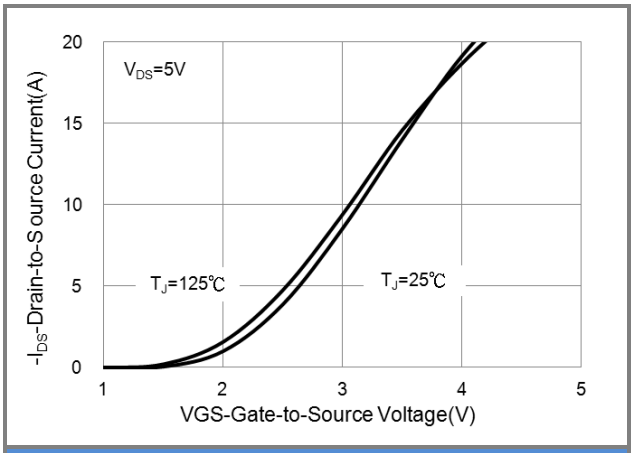


Fig.2 Transfer Characteristics

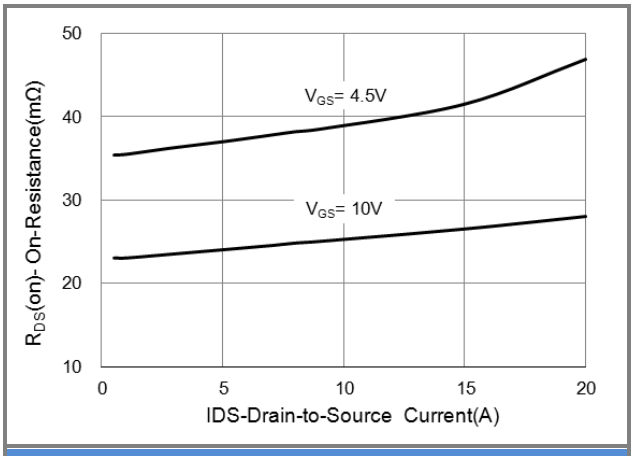


Fig.3 On-Resistance vs. Drain Current

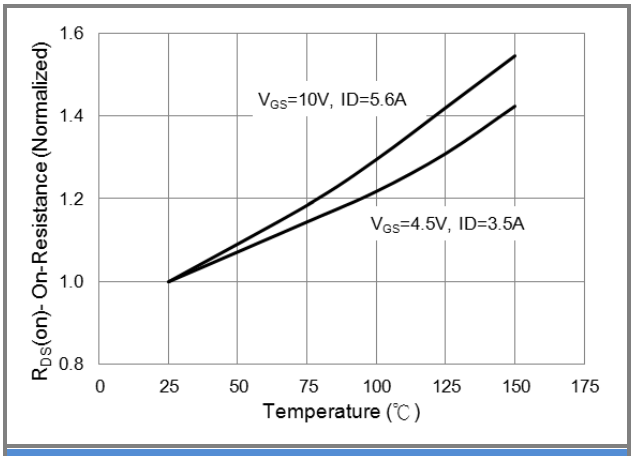


Fig.4 On-Resistance vs. Junction Temperature

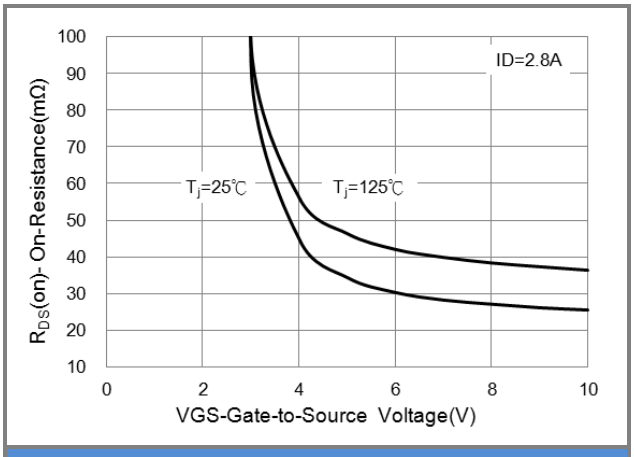


Fig.5 On-Resistance Variation with VGS.

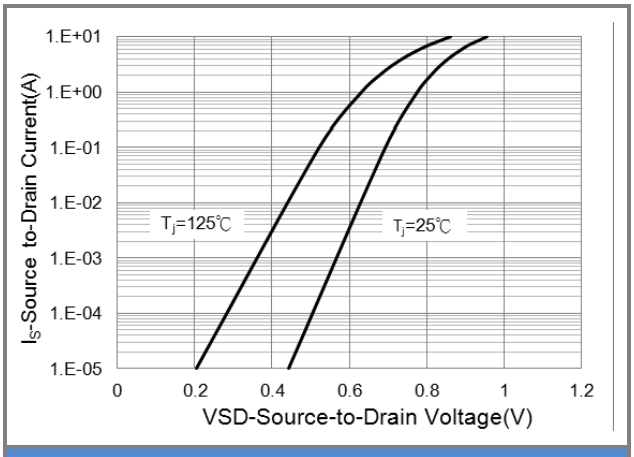


Fig.6 Body Dlode CharacterIsIcs

PJW8N03

TYPICAL CHARACTERISTIC CURVES

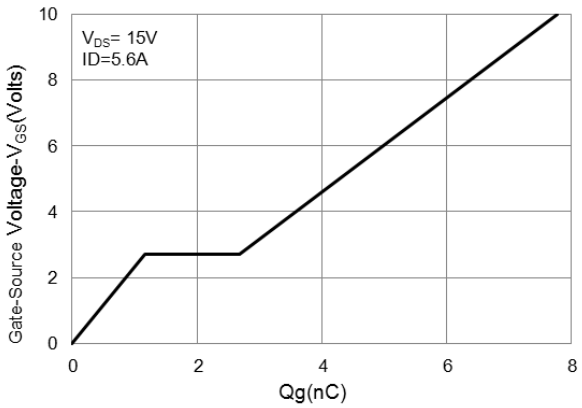


Fig.7 Gate-Charge Characteristics

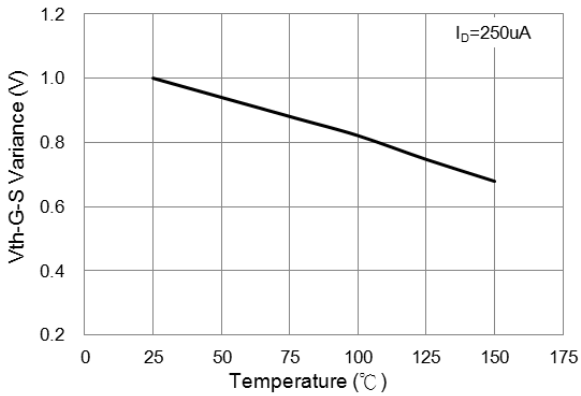


Fig.8 Threshold Voltage Variation with Temperature

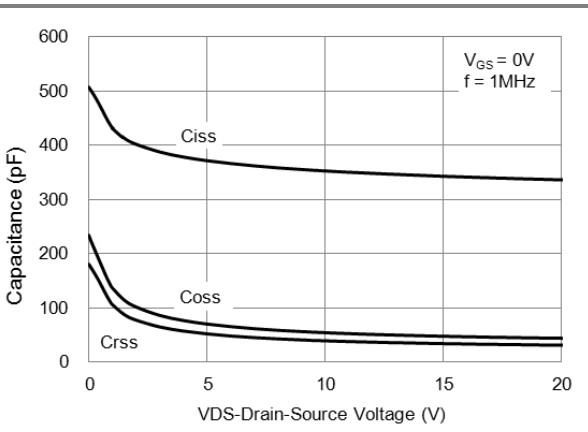
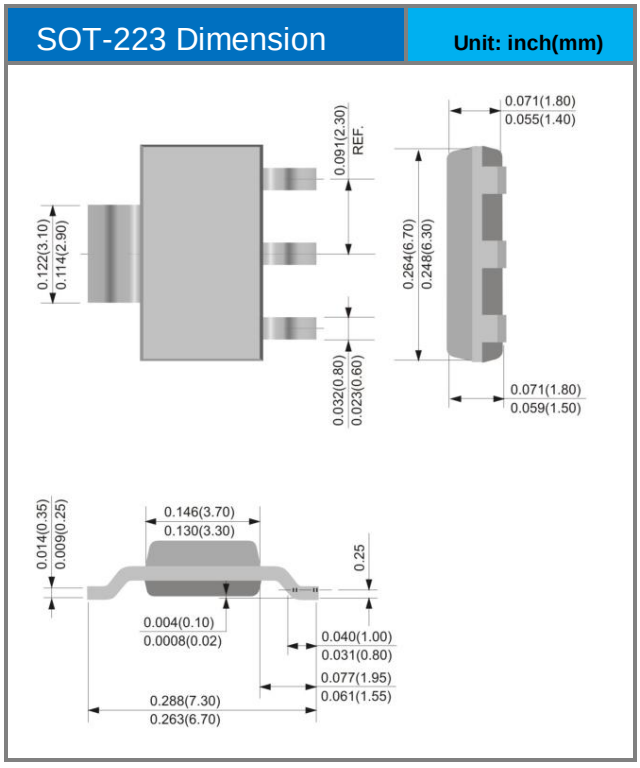


Fig.9 Capacitance vs. Drain-Source Voltage

PJW8N03

Packaging Information .

SOT-223 Dimension — Unit: inch(mm)

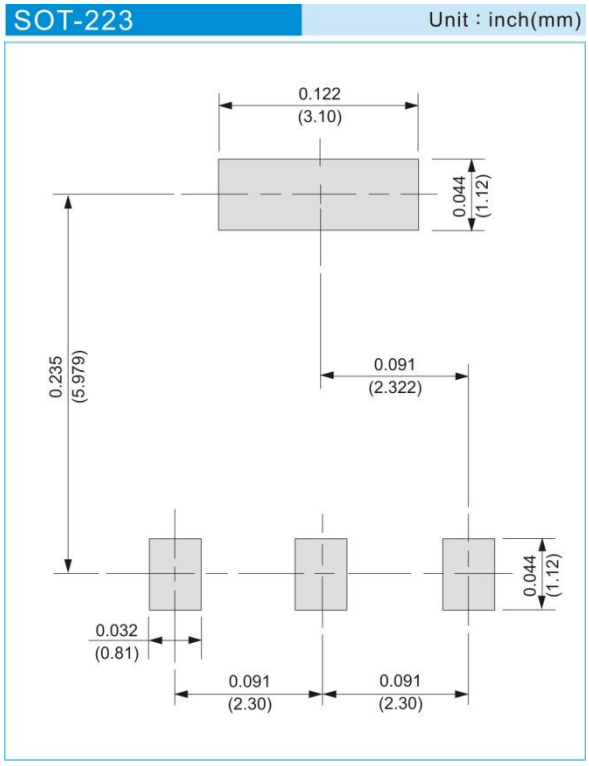


PJW8N03

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJW8N03_R2_00001	SOT-223	2,500pcs / 13” reel	W8N03	Halogen free

MOUNTING PAD LAYOUT



PJW8N03

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