



50V N-Channel Enhancement Mode MOSFET - ESD Protected

Voltage 50 V Current 360mA

Features

- RDS(ON), VGS@10V, ID@500mA<1.45Ω
- RDS(ON), VGS@4.5V, ID@200mA<1.95Ω
- RDS(ON), VGS@2.5V, ID@100mA<4.0Ω
- RDS(ON), VGS@1.8V, ID@10mA<6.0Ω
- Advanced Trench Process Technology
- ESD Protected 2KV HBM
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

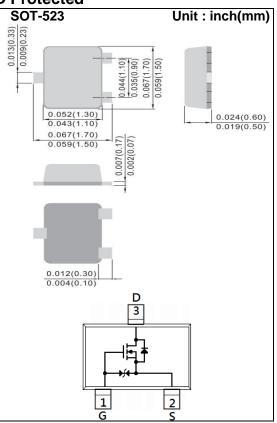
Mechanical Data

• Case: SOT-523 Package

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.00007 ounces, 0.002 grams

Marking: E38



Maximum Ratings and Thermal Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	50	V
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	360	mA
Pulsed Drain Current		I _{DM}	1200	mA
Power Dissipation	T _A =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient (Note 3)		$R_{ heta JA}$	417	°C/W





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V,I _D =250uA	50	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	0.5	0.86	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V,I _D =500mA	-	1.2	1.45	Ω
		V _{GS} =4.5V,I _D =200mA	-	1.3	1.95	
		V _{GS} =2.5V,I _D =100mA	-	1.7	4.0	
		V _{GS} =1.8V,I _D =10mA	-	4.0	6.0	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =50V,V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 10	uA
Dynamic (Note 4)						
Total Gate Charge	Q_g	V _{DS} =25V, I _D =500mA, V _{GS} =4.5V	-	0.95	-	nC
Gate-Source Charge	Q_gs		-	0.34	-	
Gate-Drain Charge	Q_{gd}		-	0.32	-	
Input Capacitance	Ciss	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	-	36	-	pF
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V, f=1.0MHZ	-	11	-	
Reverse Transfer Capacitance	Crss	I=1.UIVIMZ	-	6.6	-	
Turn-On Delay Time	td _(on)	\/ 05\/ L 500 A	-	2.3	-	ns
Turn-On Rise Time	tr	V_{DD} =25V, I_{D} =500mA, V_{GS} =10V, R_{G} =6 Ω (Note 1,2)	-	20	-	
Turn-Off Delay Time	td _(off)		-	7	-	
Turn-Off Fall Time	tf	R _G =012	-	20	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	ı				500	m A
Diode Forward Current	I _S		-	-	500	mA
Diode Forward Voltage	V_{SD}	I _S =500mA, V _{GS} =0V	-	0.9	1.5	V

NOTES:

- 1. Pulse width<a>300us, Duty cycle<a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

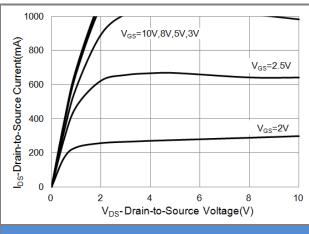


Fig.1 On-Region Characteristics

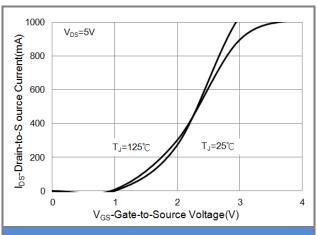


Fig.2 Transfer Characteristics

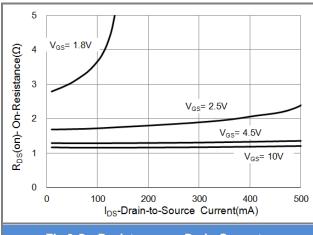


Fig.3 On-Resistance vs. Drain Current

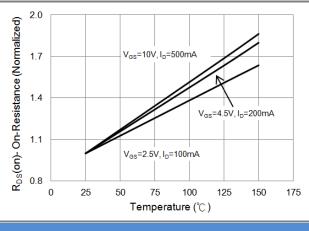


Fig.4 On-Resistance vs. Junction temperature

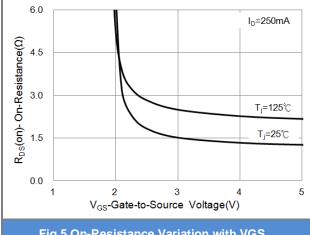
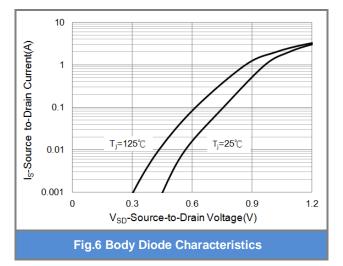


Fig.5 On-Resistance Variation with VGS.







TYPICAL CHARACTERISTIC CURVES

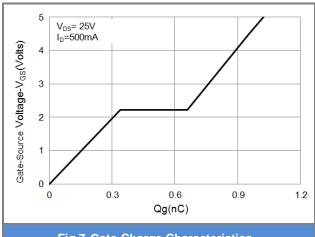
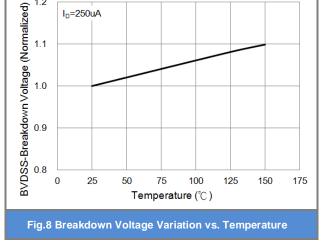
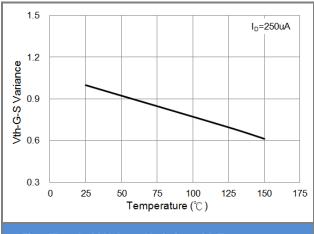


Fig.7 Gate-Charge Characteristics







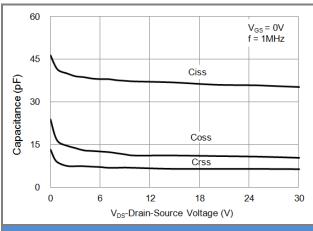


Fig.10 Capacitance vs. Drain-Source Voltage.

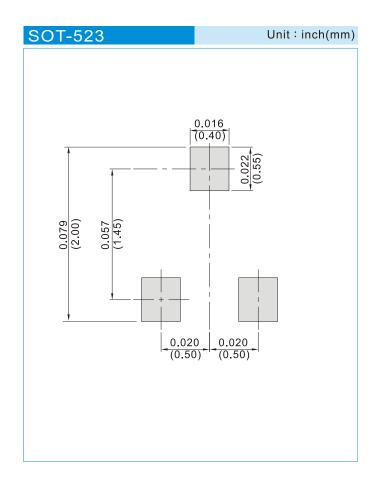




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJE8438_R1_00001	SOT-523	4K pcs / 7" reel	E38	Halogen free

MOUNTING PAD LAYOUT







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